

Title: Virtual Machine Interface For Hardware Reconfigurable And Software Programmable Processors

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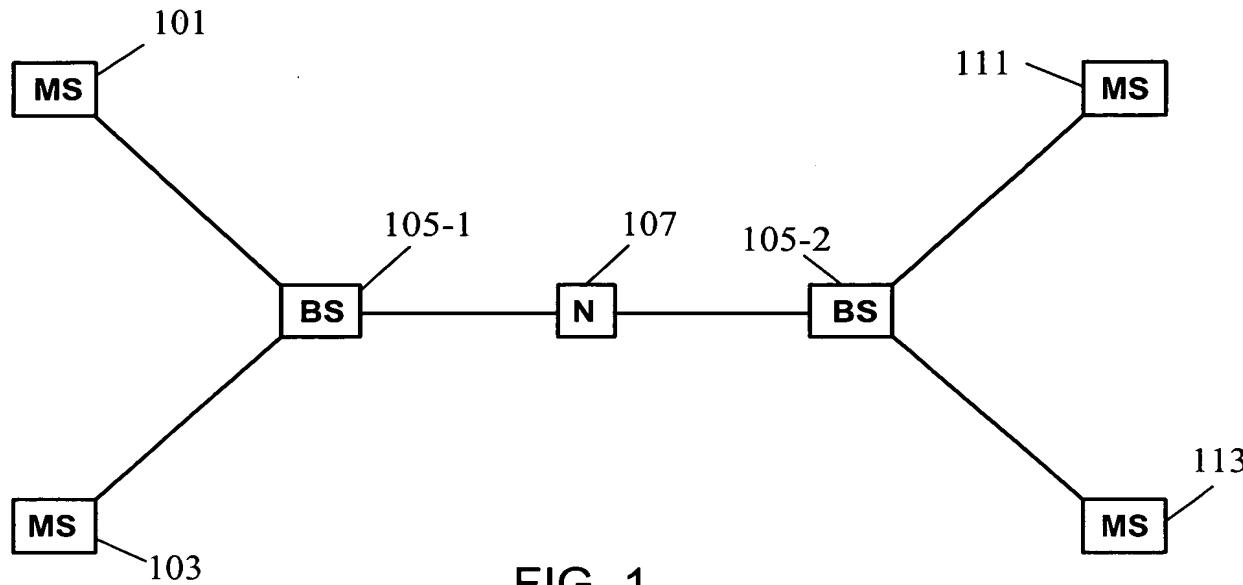


FIG. 1

PCT/US2003/030050

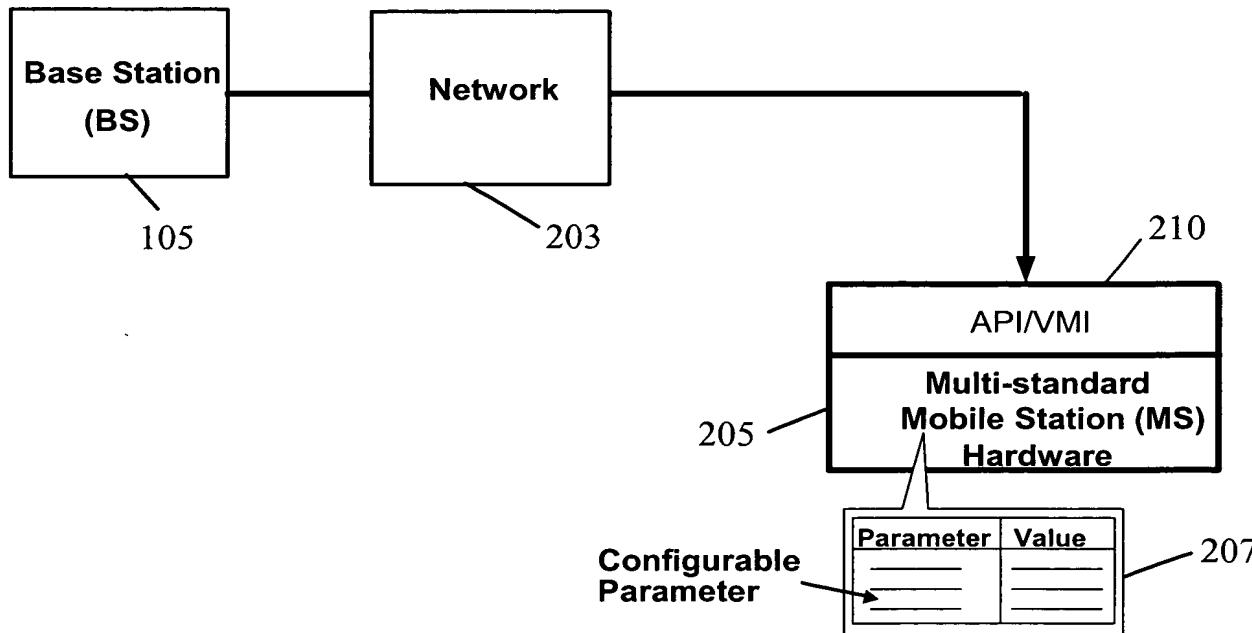


FIG. 2

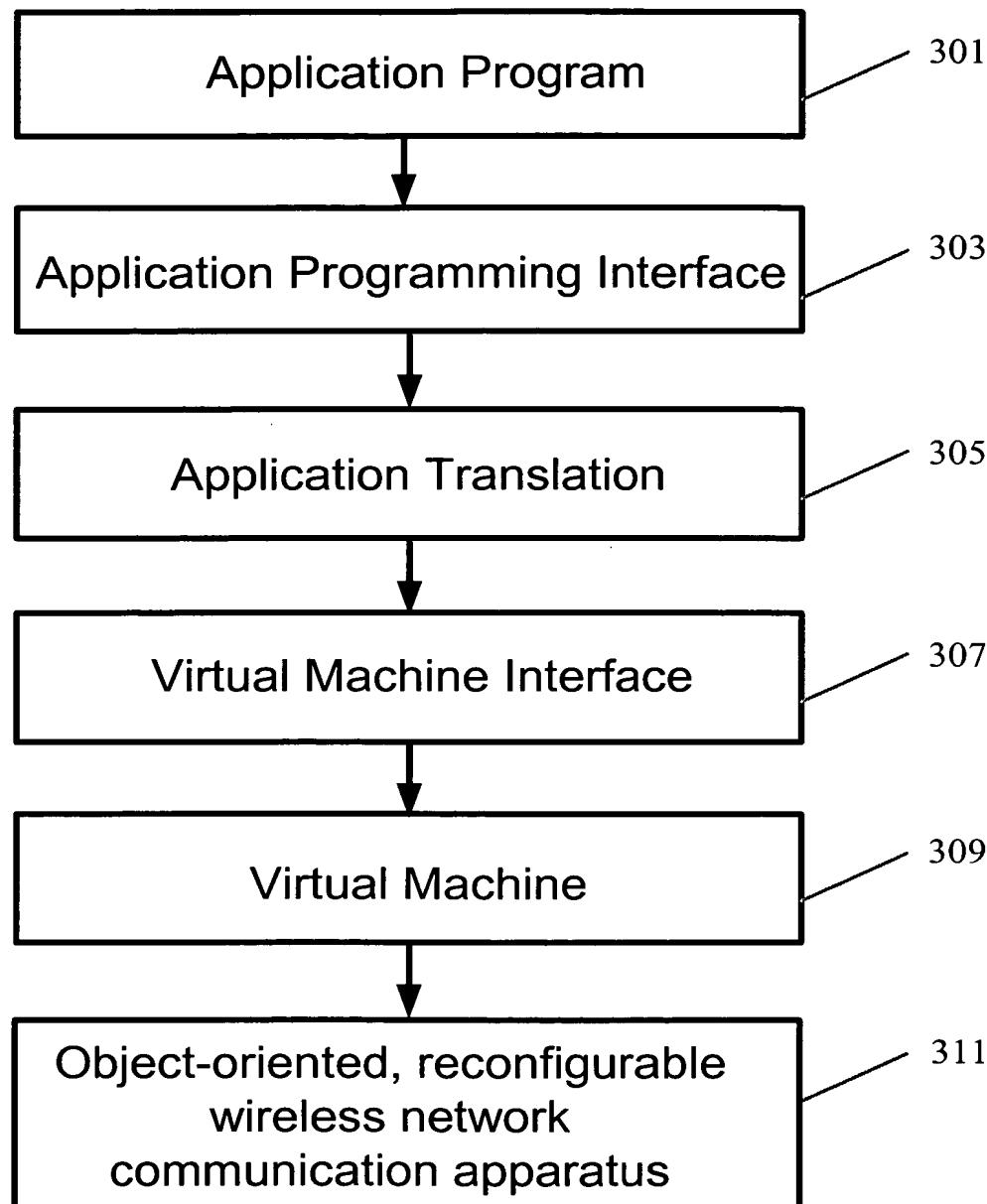


FIG. 3

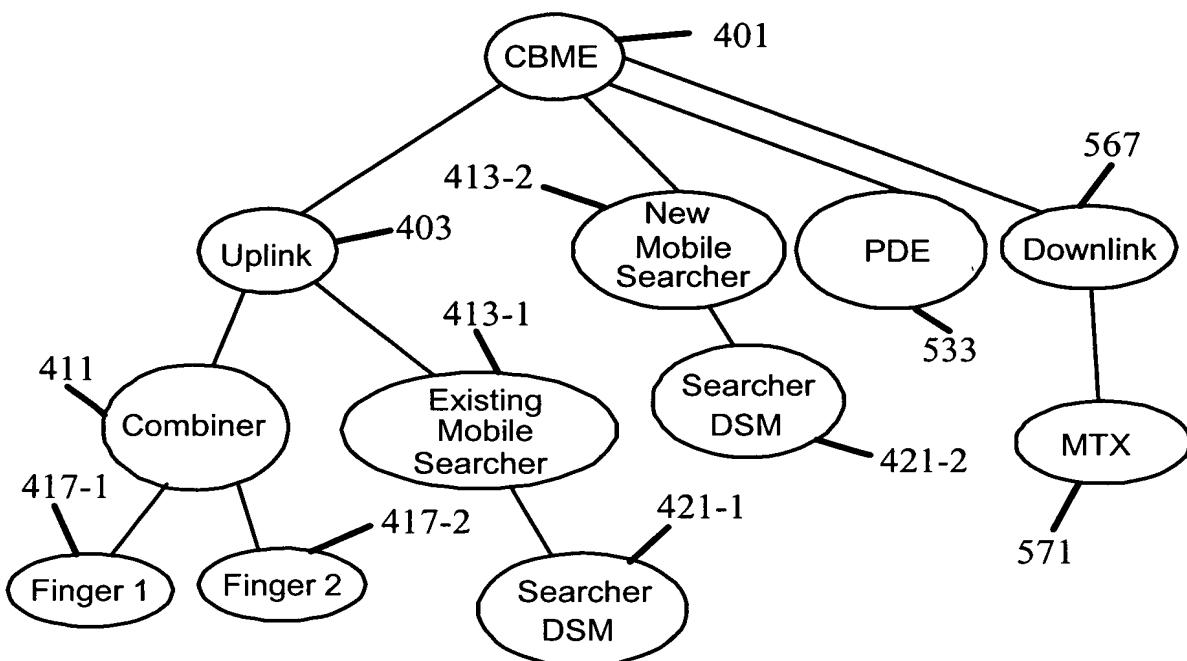


FIG. 4

PCT/US2013/042160

CBME	401
CBME_New	503
CBME_Set_User-Data	505
CGU	507
CGU_New	509
CGU_Set_User_Data	511
Searcher	413
Searcher_New	515
Set_Searcher_Static_Attributes	517
Set_Searcher_Dynamic_Attributes	519
Searcher_Start	521
Searcher_Stop	523
Assign_DSM_To_Searcher	525
Searcher DSM	421
Searcher_DSM_New	529
Set_DSM_State_Attributes	531
Preamble Detection Engine (PDE)	533
PDE_New	535
PDE_Add_Antenna	537
Finger	417
Finger_New	541
Set_Finger_Static_Attributes	543
Set_Finger_Dynamic_Attributes	545
Finger_Start	547
Finger_Stop	549
Combiner	411
Combiner_New	553
Add_Finger_To_Combiner	555
Remove_Finger_From_Combiner	557
Combiner_Start	559
Combiner_Stop	561
Uplink	403
Uplink_New	565
Downlink	567
Downlink_New	569
Tx Multicode Channel (MTX)	571
MTX_New	573

FIG. 5

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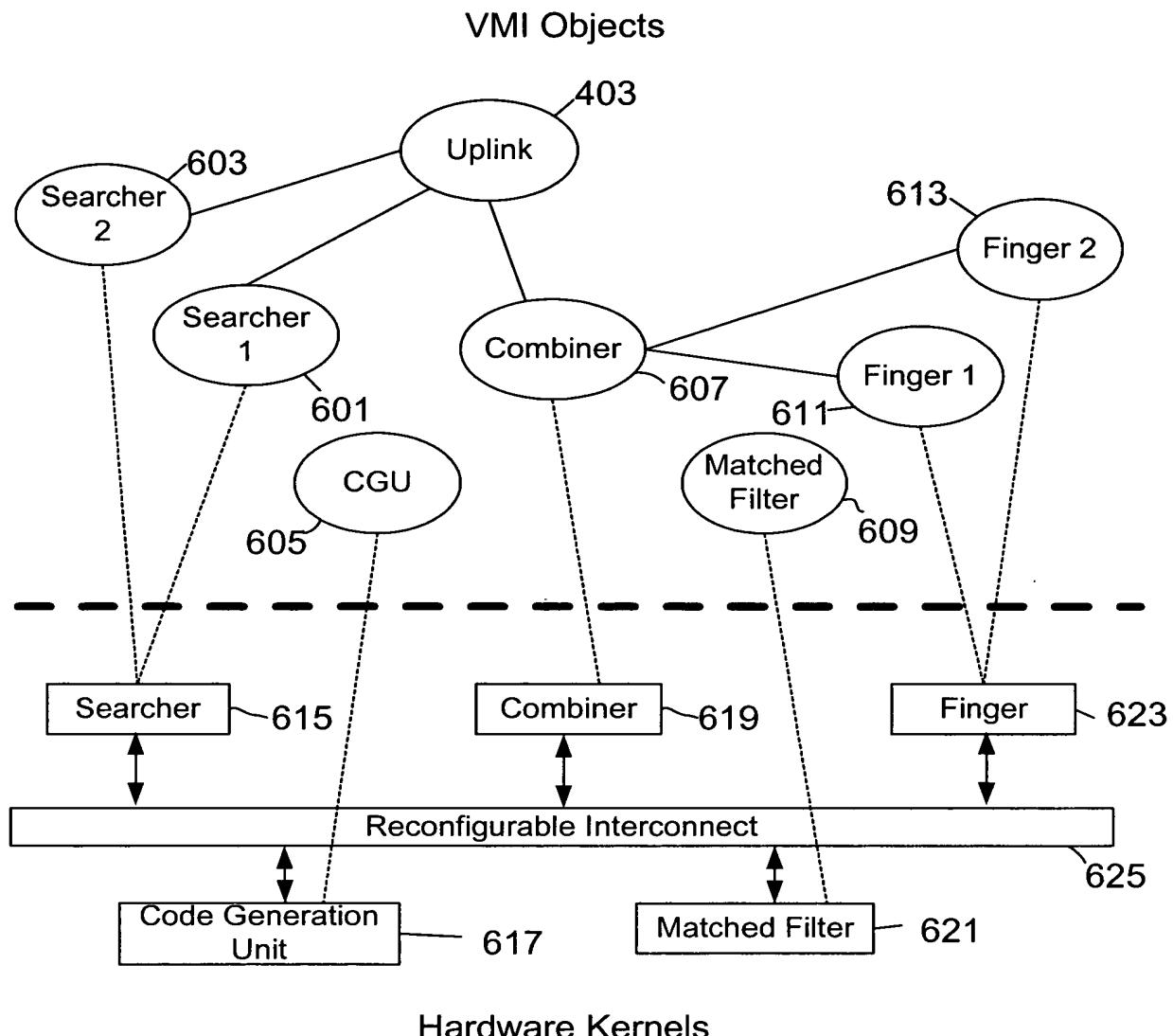


FIG. 6

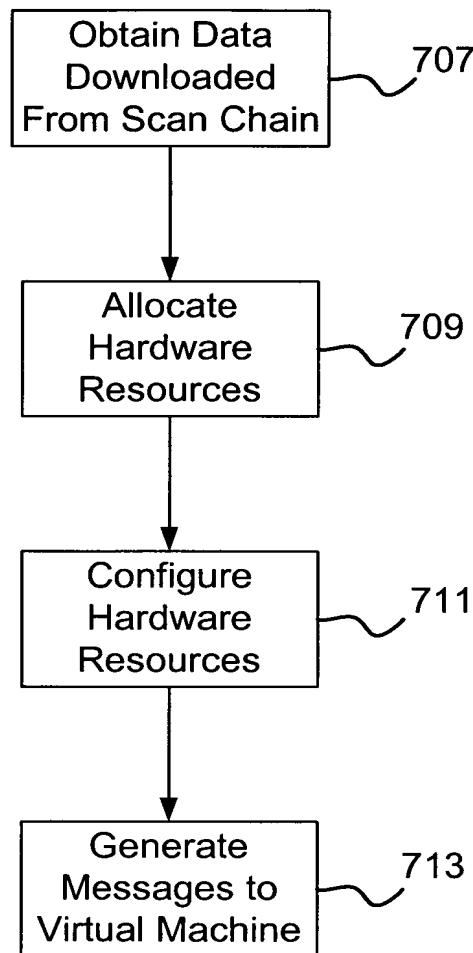
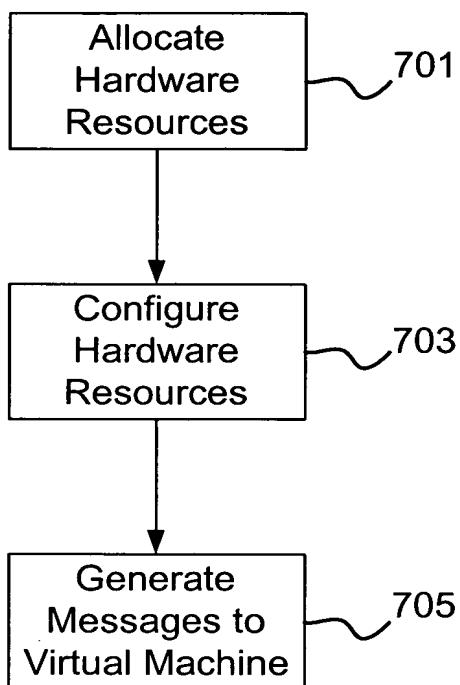


FIG. 7(A)

FIG. 7(B)

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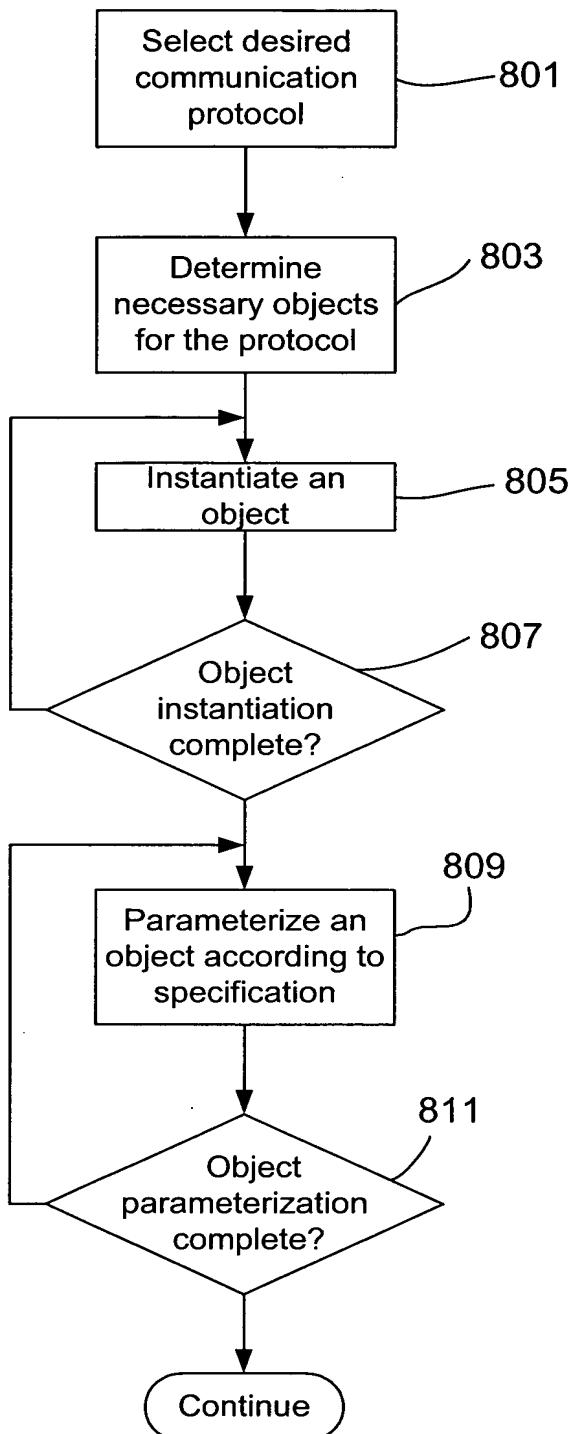


FIG. 8

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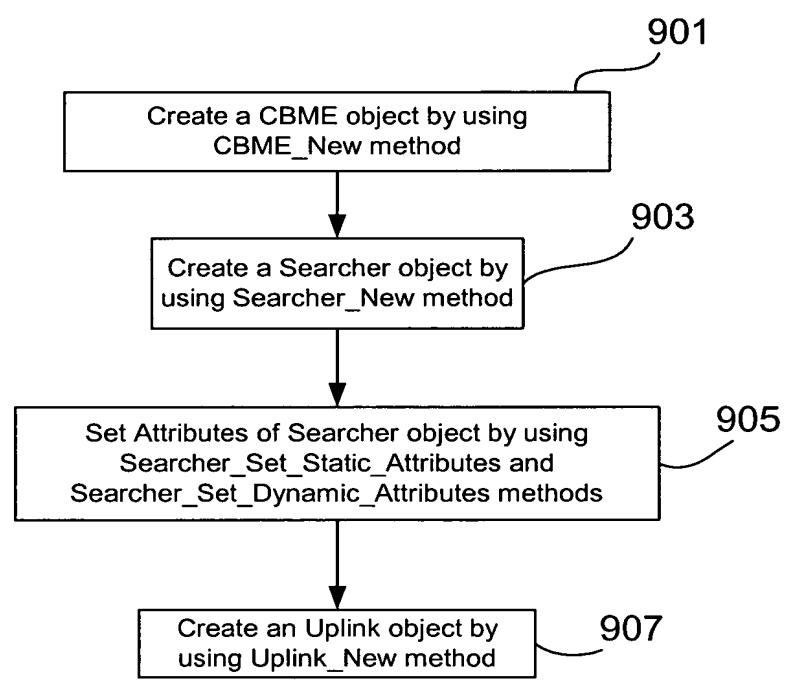


FIG. 9

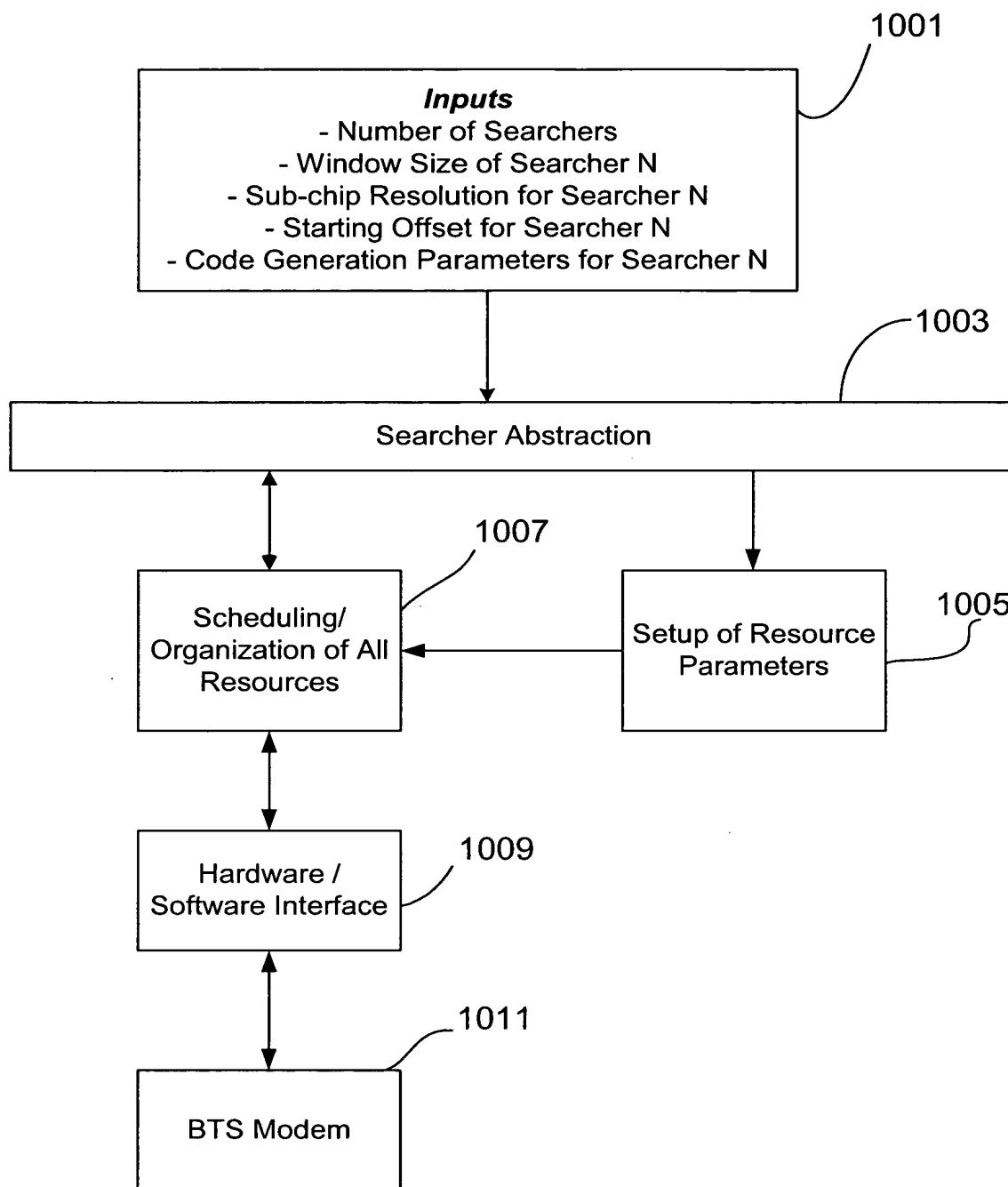


FIG. 10

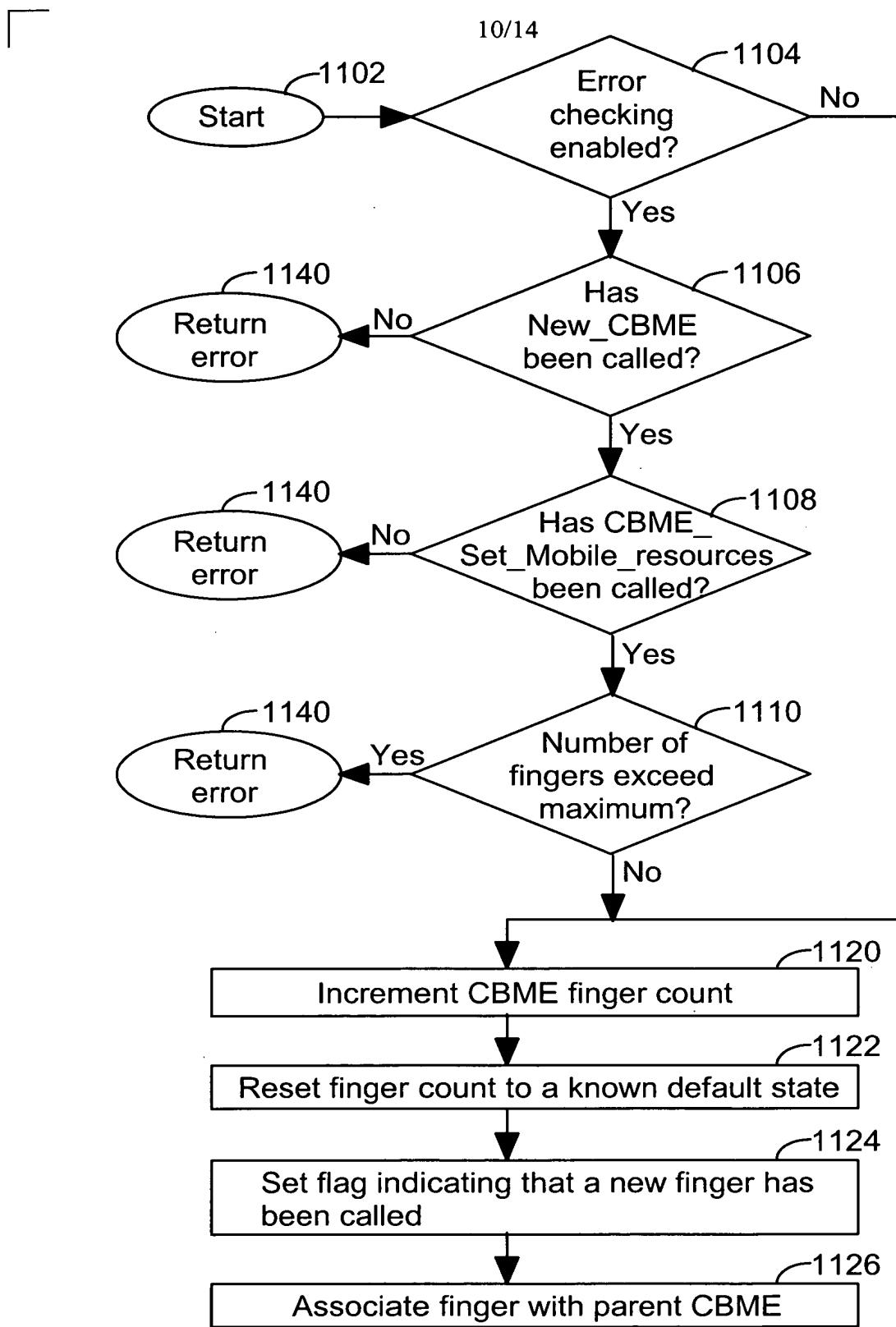


FIG. 11

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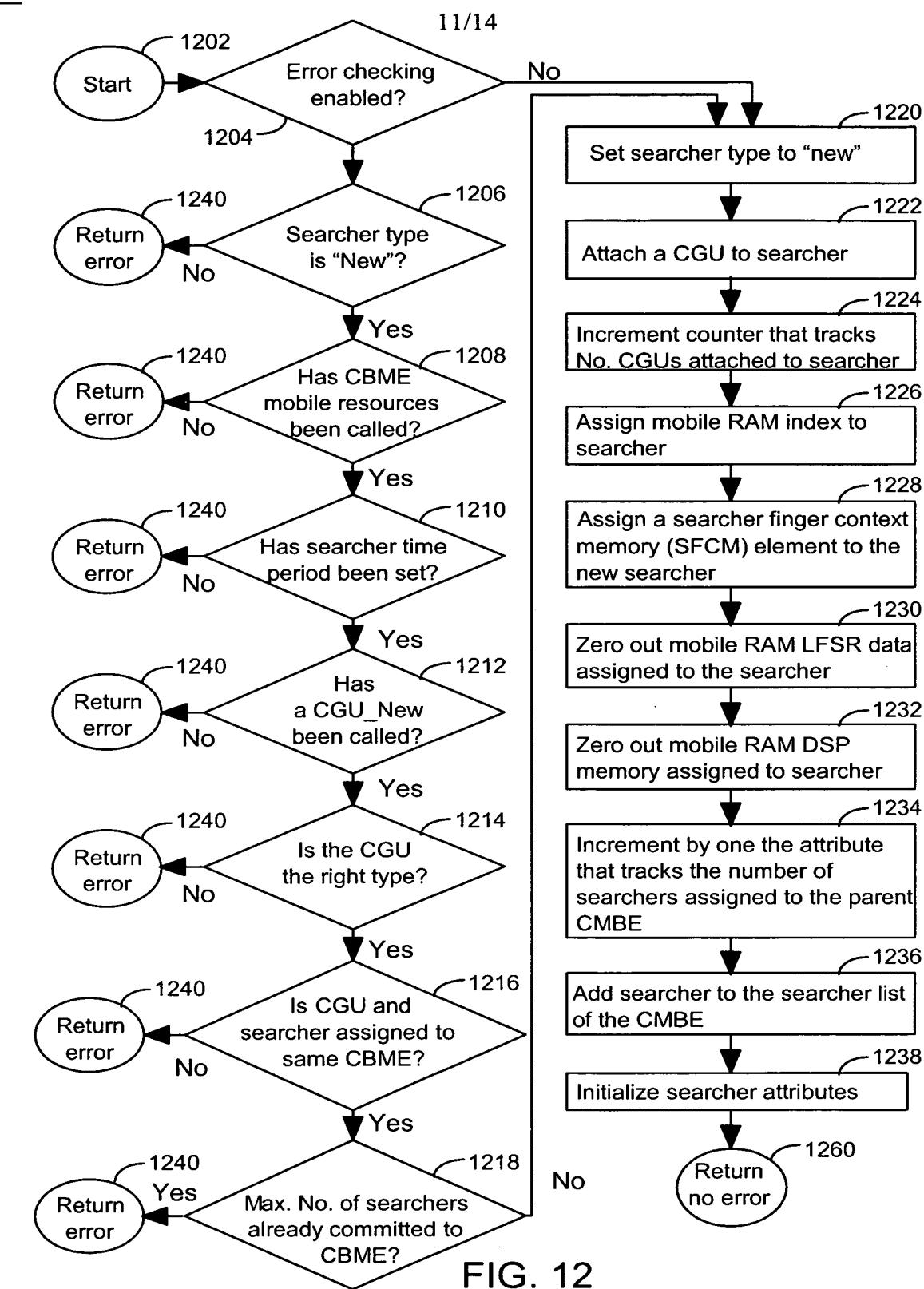


FIG. 12

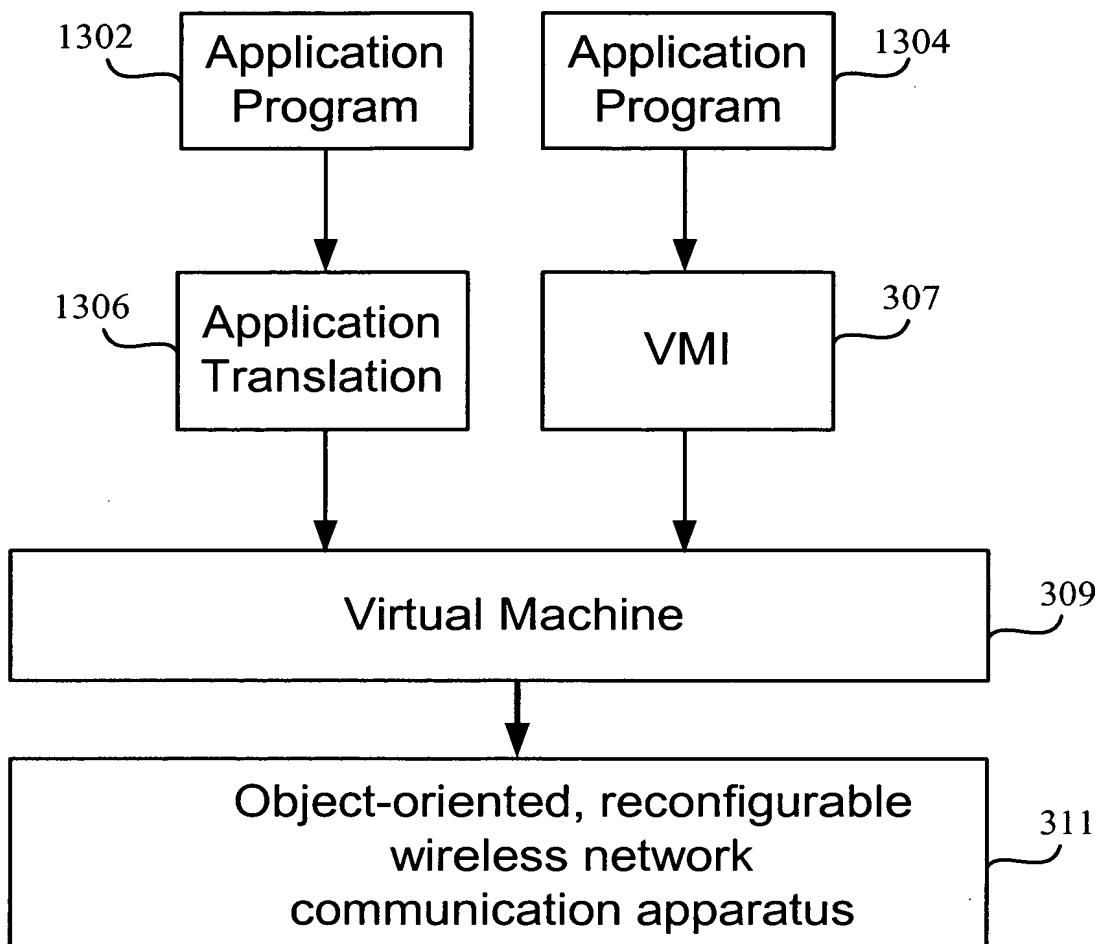


FIG. 13

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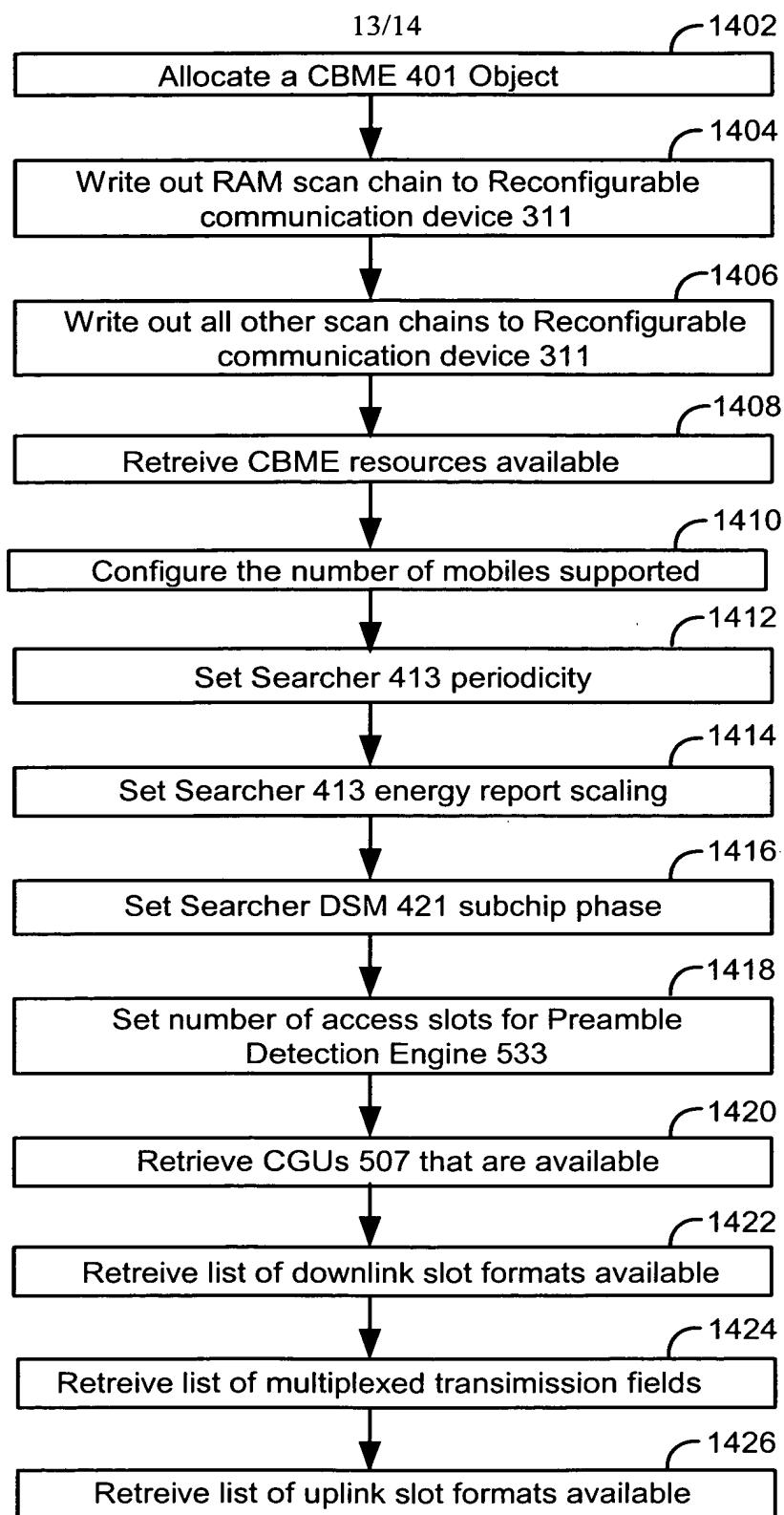


FIG. 14

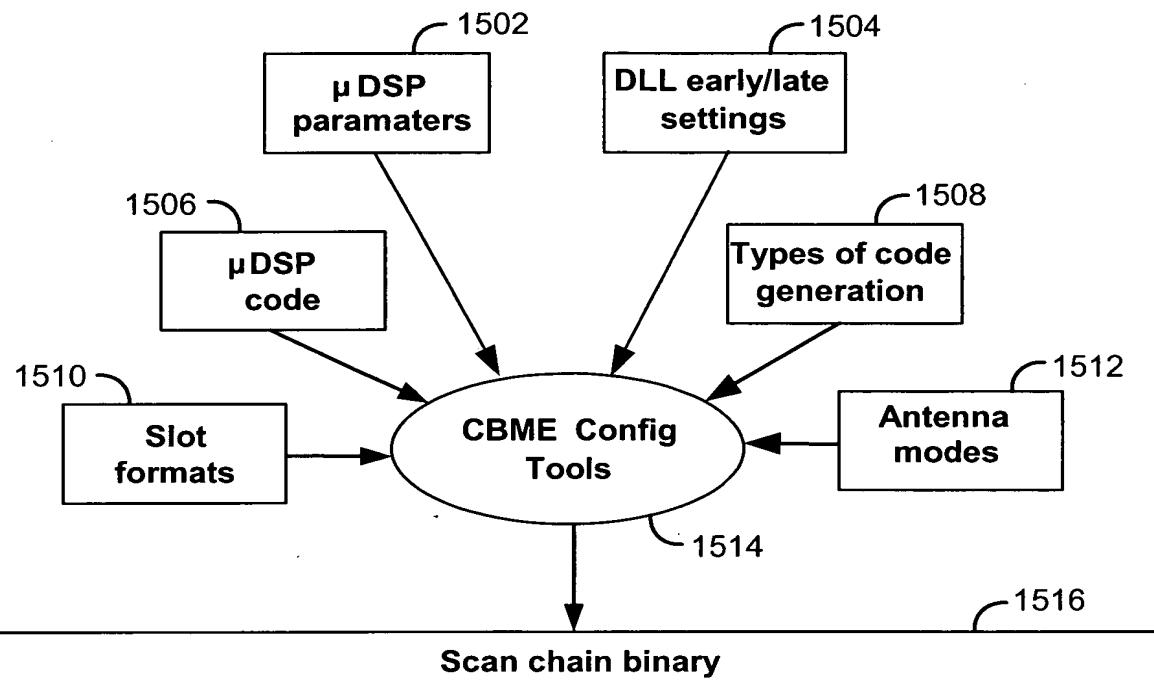


FIG. 15